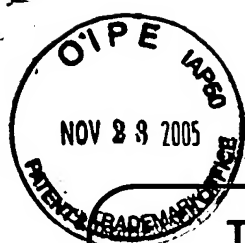


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TRANSMITTAL FORM (to be used for all correspondence after initial filing)	Application Number	10/614,403	
	Filing Date	07/07/2003	
	First Named Inventor	Rosen, Eitan	
	Art Unit		
	Examiner Name		
Total Number of Pages in This Submission		Attorney Docket Number	MP0278

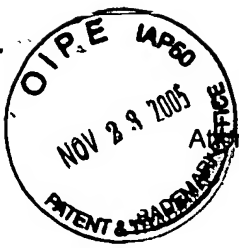
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Attorney Docket No. MP0278

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventors: Rosen, Eitan

Group Art Unit:

Serial No.: 10/614403

Examiner:

Filed: 07/07/2003

Title: Integrated Circuit Test Using Clock Signal Modification

CERTIFICATE OF MAILING

I hereby certify that this correspondence, and the documents attached hereto, are being deposited with the United States Postal Service as First Class mail in an envelope addressed to Assistant Commissioner for Patents, Alexandria, VA 22313-1450 on this date.

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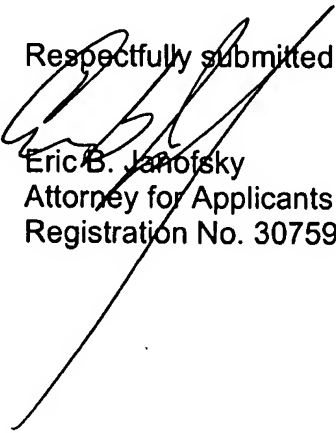
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Applicants request that the U.S. Patent and Trademark Office update the Customer Number in its records in the above-identified application to:

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Respectfully submitted,


Eric B. Janofsky
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Registration No. 30759

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